

Form PTO-1449 (Modified)	U.S. Department of Commerce Patent and Trademark Office		Atty. Docket No. Intel/18683	Serial No. 10/807,668
<b>INFORMATION DISCLOSURE STATEMENT</b> <small>(Use separate sheets if necessary)</small>		Applicants Zimmer et al.		
		Filing Date 3/23/04	Group Art Unit Unknown	

**1. U.S. PATENT DOCUMENTS**

*EXAMINER INITIALS		DOCUMENT NUMBER	ISSUE DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

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**OTHER DOCUMENTS** (Including Author, Title, Date, Pertinent Pages, etc.)

SW		Kung, Ling-Pei, "Introduction to Error Correcting Codes" Hitachi Advanced Research Laboratory, 1992.
SW		"Memory Glossary" [online], [retrieved on 01/31/2004]. CST, Inc, 2004. Retrieved from the Internet <URL: <a href="http://www.simmtester.com/page/memory/show_glossary.asp?E">http://www.simmtester.com/page/memory/show_glossary.asp?E</a> >.

EXAMINER <i>/Sean Weinman/</i>	DATE CONSIDERED <i>08/15/2006</i>
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**INFORMATION DISCLOSURE  
STATEMENT BY APPLICANT**  
 (Not for submission under 37 CFR 1.99)

Application Number	10807668
Filing Date	2004-03-24
First Named Inventor	Vincent J. Zimmer
Art Unit	2115
Examiner Name	Thomas C. Lee
Attorney Docket Number	20002/18683

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		Attorney Docket Number	20002/18683

SW	1	Extensible Firmware Interface Specification, Intel Corporation, Version 1.10, December 1, 2002, 1084 pages.	<input type="checkbox"/>
SW	2	Extensible Firmware Interface Specification, Version 1.10, Specification Update, Intel Corporation, Version -001, November 26, 2003, 63 pages.	<input type="checkbox"/>

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